

**BHARATI VIDYAPEETH'S  
COLLEGE OF ENGINEERING, LAVALE, PUNE-412115  
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**

**TIME TABLE (2019-2020)**

W.E.F.:- 15 /06/2019

SE [E & TC] SEM- I

Class Room: 128

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
9:30 to 10:30	EDC(A) DSA(B)	EDC	DE	DE	SS(TUT)	Department Review Meeting
10:30 to 11:30		SS	DSA	DSA	ECM	Department Activity
11:30 to 12:15	<b>RECESS(LUNCH BREAK)</b>					
12:15 to 1:15	ECM	EMIT(A) ECM(B)	EMIT	DE(B) ECM(A)	DE(A) EMIT(B)	
1:15 to 2:15	EDC		EDC			
2.15 to 2.30	<b>( BREAK)</b>					
2.30 to 3.30	DSA	DE	DSA(A) EDC(B)	SS	DSA	
3.30 to 4.30	SS	ECM		EDC	DE	

**GFM – P. A.Yadav**

Sr No	Faculty Name	Class & SEM	Subject Name	L	P	T	LAB
1	Prof.S.G.Gawhale	S.E.(E& TC) Sem-I	EDC	04	04	---	Analog Circuit Lab
			ECM	---	04	---	Electrical Lab
2	Prof.L.P.Chaudhari	S.E.(E& TC) Sem-I	EMIT	01	04	---	Communication Lab
3	Prof.L.K.Wani	S.E.(E& TC) Sem-I	DE(A)	---	02	---	Digital Circuit Lab
3	Prof.P.A.Yadav	S.E.(E& TC) Sem-I	DSA	04	04	---	Simulation Lab
			DE (B)	---	02	---	Digital Circuit Lab
4	Prof.Ankita Sawalkar	S.E.(E& TC) Sem-I	ECM	03	---	---	
			SS	03	---	01	
5	Prof.M.H.Molawade	S.E.(E& TC) Sem-I	DE	04	---	---	



**Prof.L.P.Chaudhari  
Time Table In-charge**



**Prof.A.B.Wani  
H.O.D.(E & TC)**

**Head**  
Dept. of Electronics & Telecommunication  
Engineering  
Bharati Vidyapeeth's  
College of Engineering,  
Lavale, Pune - 412 115.

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**TIME TABLE (2019-2020)**

**W.E.F.:- 15 /6/2019**

**TE [E & TC] SEM- I**

**Class Room: 129**

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
9:30 to 10:30	EMT	DSP	$\mu$ C	DC(A) ESD(B)	DC	Department Review Meeting
10:30 to 11:30	DC	Library Hour	ESD		DSP	Department Activity
11:30 to 12:15	<b>RECESS(LUNCH BREAK)</b>					
12:15 to 1:15	ESD(A) DSP(B)	Mechatronics	Mechatronics	ESD	$\mu$ C	
1:15 to 2:15		EMT	EMT	EMT(T)	Mechatronics	
2.15 to 2.30	<b>( BREAK )</b>					
2.30 to 3.30	$\mu$ C	Mechatronics(A) $\mu$ C(B)	DC	DSP(A) Mechatronics(B)	$\mu$ C(A) DC(B)	
3.30 to 4.30	Sports		DSP			

**GFM – Prof. L. P. Chaudhari**

Sr No	Faculty Name	Class & SEM	Subject Name	L	P	T	LAB
1	Prof.A.B.Wani	T.E.(E& TC) Sem-I	ESD	2	04	-	Digital Circuit Lab
2	Prof.L.K.Wani	T.E.(E& TC) Sem-I	DSP	3	04	-	Simulation Lab
3	Prof.S.G.Gawhale	T.E.(E& TC) Sem-I	$\mu$ C	3	04	-	Simulation Lab
4	Prof.L.P.Chaudhari	T.E.(E& TC) Sem-I	DC	3	04	-	Communication Lab
5	Prof.P.A.Yadav	T.E.(E& TC) Sem-I	Mechatronics	3	04	-	Mechatronics Lab
6	Prof.Ankita Sawalkar	T.E.(E& TC) Sem-I	EMT	3	-	1	



**Prof.L.P.Chaudhari**  
**Time Table In-charge**



**Prof.A.B.Wani**  
**H.O.D.(E & TC)**

**Head**  
of Electronics & Telecommunication  
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**TIME TABLE (2019-2020)**

**W.E.F.:- 15 /6/2019**

**BE [E & TC] SEM- I**

**Class Room: 122**

Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
9:30 to 10:30	RMT	EPD	CN	VLSI	PROJECT	Department Review Meeting
10:30 to 11:30	EPD	CN	VLSI	RMT	PROJECT	Department Activity
11:30 to 12:15	<b>RECESS(LUNCH BREAK)</b>					
12:15 to 1:15	CN	VLSI(A)	RMT (A)	CN(A)		
1:15 to 2:15	ES & RTOS					
2.15 to 2.30	<b>( BREAK )</b>					
2.30 to 3.30	ES & RTOS(A)	VLSI	EPD	ES & RTOS		
3.30 to 4.30		ES & RTOS	RMT	CN		

**GFM – Prof. L.K.Wani**

Sr No	Faculty Name	Class & SEM	Subject Name	L	P	T	LAB
1	Prof.A.B.Wani	B.E.(E& TC) Sem-I	EPD	3	--	--	
2	Prof.L.K.Wani	B.E.(E& TC) Sem-I	VLSI	3	2	--	Simulation Lab
3	Prof.L.P.Chaudhari	B.E.(E& TC) Sem-I	ES & RTOS	3	2	--	Simulation Lab
4	Prof.	B.E.(E& TC) Sem-I	CN	4	2	--	Computer Lab
			RMT	3	2	--	Communication Lab



**Prof.L.P.Chaudhari  
Time Table In-charge**



**Prof.A.B.Wani  
H.O.D.(E & TC)  
Head**

Department of Electronics & Telecommunication  
Engineering  
Bharati Vidyapeeth's  
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