Hon'ble Dr. Patangraoji Kadam Saheb
Founder, Bharati Vidyapeeth

BLESSINGS

Bharati Vidyapeeth's

College of Engineering for Women, Pune

Participation of women in technology is an important aspect in social and economic development of the nation. It is a critical constituent in the process of improving the quality of life of women themselves. When women have economic empowerment, it is a way for others to see them as equal members of society. Through this, they achieve more self-respect and confidence by their contributions to their communities. As women play key roles in social transformation, Hon'ble Dr. Patangraoji Kadam Saheb established Bharati Vidyapeeth's College of Engineering for Women, Pune in June 2000 with the vision, "Women empowerment through Technical Education" and provided opportunity to women for higher education in the field of technology. The institute was started exclusively for women and it is running with 100% women students. Establishing and running Women Engineering College really contributes to social transformation through dynamic education, which is the vision of Bharati Vidyapeeth.

Bharati Vidyapeeth's College of Engineering for Women, Pune

Pune-Satara Road, Dhankawadi, Pune 411043
Recognized by AICTE, New Delhi, DTE Mumbai, Affiliated to Savitribai Phule Pune University
Accredited by NAAC
Id No.: PU/PN/Engg./150/2000, DTE College Code: EN6285
Phone: (020)24371684, (020)24361732 Fax: (020) 24372210
Email: coewpune@bharatividyapeeth.edu, Website: http://coewpune.bharatividyapeeth.edu

Undergraduate Programme

Sr. No.	Course	Intake	Course Code
1	B.E. Electronics and Telecommunication Engg. (E & TC)	120	628537250F
2	B.E. Computer Engg. (CE)	120	628524550F
3	B.E. Information Technology (IT)	60	628524650F

Post Graduate Programme

Sr. No.	Course	Intake	Course Code
1	M.E. (E & TC-VLSI & Embedded System)	9	628534150F

Vision:

Women Empowerment through Technical Education

Mission:

- Develop women students to rise to their full potential.
- Impart knowledge and prepare competent engineers.

Special Features:

1. Received “Best Women College of the Year 2019” Award.
2. Recipient of “College of Substance” Award.
3. The oldest engineering college “exclusively for women”.
4. All government scholarships are applicable for eligible students.
5. Placement opportunities in multinational companies with 100% assistance.
6. Excellent university results and tradition of consistent university rank holders.
7. MOUs with reputed industries and academia.
8. On campus hostel facility with 24 × 7 security.
9. DTE approved e-Scrutiny centre for admissions.

Facebook: https://www.facebook.com/Bharati-Vidyapeeth-College-of-Engineering-for-Women-Pune-1599060517007121
Instagram: https://instagram.com/bvcoew_pune?igshid=ep1a85ikhj6s

Principal's Message

Prof. Dr. P. V. Jadhav

Principal

Dear Stakeholders,

Greetings from BVCOEW, Pune!

I hope you are all well and in good health. I am delighted to announce that BVCOEW is celebrating its Silver Jubilee this year, a significant milestone that reflects our dedication to excellence in education and our enduring impact on the community.

I am very pleased to release the e-newsletter "Blessings..." Volume 6, Issue 2, A.Y. 2023-24. This e-newsletter serves as an excellent medium for showcasing the technical endeavours and accomplishments of both students and faculty throughout the semester. I am particularly content and glad that we were able to organise so many technical events for our students during this period. Participation in these activities enriches students' educational experiences and contributes to their personal growth.

I highly appreciate the team efforts of the Coordinator, Prof. Dr. Deepali Godse, all Chief Editors, and Editors for making this e-newsletter a grand success. I also extend my best wishes to the students' editorial team. The e-newsletter is the outcome of great teamwork, and we always believe that "Alone, we can do so little; together, we can do so much".

Internal Quality Assurance Cell (IQAC)

IQAC Objectives:

- To imbibe quality environment at institute in all academic and administrative processes.
- To be instrumental in review of teaching learning process, structures, methodologies and student centric methods for achieving best educational environment.

Roles and responsibilities of IQAC:

- Keeping regular updates of NAAC and other quality improvement circulars.
- Conducting regular meetings of IQAC.
- Preparing Strategic plan of the institute.
- Preparation and submission of Annual Quality Assurance Report (AQAR) yearly.
- Maintaining academic records and conducting various audits at required intervals.
- Taking review of updating and updation of hardware and software requirements and internet facilities.
- Updating feedback forms as per guidelines from regulatory bodies.
- Providing guidelines for implementing ERP.
- Organizing various technical and nontechnical events.
- Preparation of reports of various activities for quality improvement.

Members List:

Sr. No.	Name of the IQAC Member	Designation	Position
1	Dr. P. V. Jadhav	Head of the Institute	Chairperson
2	Dr. S. R. Patil	HOD, E & TC	Teacher Representative
3	Prof. D. D. Pukale	HOD, Computer Engineering	Teacher Representative
4	Dr. D. A. Godse	HOD, Information Technology	Teacher Representative
5	Dr. A. M. Pawar	HOD, Engineering Sciences and Allied Engineering	Teacher Representative
6	Dr. K. A. Malgi	ICT & IT Infrastructure Coordinator Member	Teacher Representative
7	Prof. Thorat S.M.	Alumni Coordinator	Teacher Representative
8	Prof. P. D. Kale	Placement cell Coordinator	Teacher Representative
9	Dr. V. R. Pawar	Academic & Research Coordinator	Teacher Representative
10	Dr. S. M. Rajbhoj	Industry institute Interaction	Teacher Representative
11	Prof. S.T. Khot	Professor E & TC	Teacher Representative
12	Dr. K. D. Jadhav	Joint Secretary of Bharati Vidyapeeth	Member of Management
13	Dr. S. F. Patil	Executive Director of Bharati Vidyapeeth	Member of Management
14	Mrs. V. S. Kadam	Office Superintendent	Admin. Representative
15	Dr. V. M. Mohite	Librarian	Admin. Representative
16	Ms. Shital Patil	Alumni	Admin. Representative
17	Ms. Khushi Pramod Mittal	Student	Student Representative
18	Mr. Nitynand Tendulkar	Industry	Industry Representative
19	Mr. Sanjaykumar Gupta	Parent	Parent Representative
20	Dr. S. S. Chorage	Professor E & TC	IQAC Coordinator, NAAC Central Coordinator

From the Desk of Coordinator...

Prof. Dr. D. A. Godse
Newsletter Coordinator

Dear Students, Faculty, Staff, Alumni, Employers, Parents and Well-wishers,
Greetings!

We are very fortunate to have the blessings of our honourable founder, Dr. Patangraoji Kadam Saheb, with us at Bharati Vidyapeeth forever.

As we embark on a remarkable milestone, our esteemed institution, Bharati Vidyapeeth's College of Engineering for Women, Pune, is celebrating 25 years of excellence in technical education for empowering women. It gives me immense pleasure to welcome you to this edition of our semester-wise e-newsletter. This semester, we continue to uphold our commitment to fostering a vibrant academic environment enriched with technical activities that propel our students towards becoming future leaders and innovators. From insightful seminars to hands-on workshops, and from cutting-edge research to industry collaborations, every endeavor undertaken at BVCOEW, Pune, reflects our unwavering dedication to nurturing talent and pushing the boundaries of knowledge.

I would like to extend my sincere thanks to our Principal, Prof. Dr. Pradeep Jadhav, all Heads of Department, faculty, staff and beloved students for their involvement in organizing and conducting various technical activities. I truly appreciate dedicated and systematic efforts of the editorial team members of the e-newsletter.

Thank you everyone for being a part of our journey. Here's to 25 years of brilliance, and too many more milestones ahead!

We always believe, "Teamwork makes the dream work".

Department of Computer Engineering

Vision :

- *Pioneers in women Computer Engineering by providing competent technical knowledge and enriched social awareness.*

Mission :

- *To inculcate quality education in various domains of Computer Engineering.*
- *Encourage students, to showcase their talents and search the community needs.*
- *To improve technical competency by providing value added training.*

Programme Educational Objectives (PEOs):

- *The graduate of the program will implement strong fundamental domain knowledge to solve engineering problems with modern tools and technology.*
- *The graduate of the program will work as committed professional, demonstrating strong ethical practices with understanding of social responsibilities for betterment of society.*
- *To prepare a motivated graduate by inculcating multidisciplinary thinking through research attitude and lifelong learning.*
- *To prepare graduates with strong communication and leadership skills to work effectively as an individual as well as in teams.*

Programme Outcomes (POs):

On completion of the program students will be able to

PO1. Engineering knowledge: *Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.*

PO2. Problem analysis: *Identify, formulate, research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.*

PO3. Design/development of solutions: *Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.*

PO4. Conduct investigations of complex problems: *Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.*

PO5. Modern tool usage: *Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.*

PO6. The engineer and society: *Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.*

PO7. Environment and sustainability: *Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.*

PO8. Ethics: *Apply ethical principles and commit to professional ethics, responsibilities, and norms of the engineering practice.*

PO9. Individual and teamwork *Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.*

PO10. Communication: *Communicate effectively on complex engineering activities with the engineering community and with society, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.*

PO11. Project management and finance: *Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.*

PO12. Life-long learning: *Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.*

Programme Specific Outcomes (PSOs):

PSO1. Professional Skills: *The ability to understand, analyse and develop computer programs in the areas related to algorithms, system software, multimedia, web design, big data analytics, and networking for efficient design of computer-based systems of varying complexities.*

PSO2. Problem-Solving Skills: *The ability to apply standard practices and strategies in software project development using open-ended programming environments for betterment of society.*

PSO3. Successful Career: *Empower women with modern computer languages, environments, platforms, communication and leadership skills to build a successful career.*

HOD's Message

Prof. D. D. Pukale
Head of Computer Engineering Department

First of all, I would like to congratulate faculty members and students of newsletter team for their efforts in the newsletter formation. It is my pleasure to write message for newsletter.

Computer Engineering Department was established in the year 2000 with the objective to impart quality education in Computer Engineering. The students here are the budding engineers with right knowledge, and we propel them to the path of success in the country and abroad. The program focuses on theoretical computer science as well as software and application development. Department has well qualified and vibrant faculty dedicated for the betterment of the students. The long-term goal of the department is to harness the skills of faculty and students to create a technically sound learning environment that can be beneficial to the industry, society and in the field of technical innovation. The students passed out from the department are well placed with good packages and having multiple offers from various industries or have opted for higher studies. The department encourages students to enhance their knowledge not only from academic perspective but also from industrial perspective by undertaking various online courses available on reputed platforms such as NPTEL, Coursera etc. In addition, faculty of department are conducting workshops like "AWS certification" for introducing students to new technology.

Technical Festival 2024 “Technophilia”

Inaugural program of Technical Festival 2024 “Technophilia”

Technophilia 2024, an annual technical extravaganza, illuminated the academic calendar of Bharati Vidyapeeth’s College of Engineering for Women, Pune. This year, the event radiated with the theme "Tech-Passion Fuels Innovation for a Better Tomorrow," underscoring the pivotal role of technology in shaping our collective future. The inaugural ceremony of Technophilia 2024 was graced by esteemed personalities, adding prestige to the occasion. Hon'ble Padmashree Mrs. Lila Poonawalla, Chairperson of the Lila Poonawala Foundation, adorned the event as the Chief Guest. Additionally, Hon’ble Mrs. Swapnali Kadam, Chairperson of Bharati Vidyapeeth Rabindranath Tagore School of Excellence, Pune, honored us as the Guest of Honour. Technophilia 2024 showcased a diverse array of technical and non-technical events, creating an immersive experience for participants. The technical segment included Project Exhibition, Poster Presentation, Startup Idea Competition, Circuit Building, Coding Competition, and Technical Quiz. Complementing these, non-technical events such as Rangoli Competition, T-shirt Painting, Reel Making Competition, and a special Photo Exhibition for faculty and staff members added colors and vibrancy to the festival. Under the guidance of Prof. Dr. D. A. Godse as the Convener, and the diligent coordination of Prof. A. V. Kanade and Prof. K.V. Patil, Technophilia 2024 manifested into a grand success. Ms. Suhani Havaldar from Third Year IT Department led the organizing committee of students as the General Secretary, ably supported by Co-General Secretaries, Ms. Sanchita Sawai and Ms. Saee Datar. The allure of Technophilia 2024 resonated far and wide, as evidenced by the remarkable participation of students from various colleges. With a total of 342 registrations across all events, the festival buzzed with energy and fervor. The commitment and zeal displayed by participants, coupled with meticulous planning by the organizing committee, culminated in the resounding success of this year's edition. Technophilia 2024 stands as a testament to the power of technology and innovation in shaping a brighter tomorrow. Through a blend of technical prowess, creative expression, and collaborative spirit, the event fostered an environment conducive to learning, growth, and camaraderie. As we bid adieu to yet another memorable chapter of Technophilia, we eagerly anticipate the future, fueled by the passion for technology and the pursuit of excellence. The collective efforts of faculty and supporting staff under the great support, guidance and innovative ideas of dynamic Principal, Prof. Dr. Pradeep Jadhav proved “Teamwork makes the dream work”.

Major Technical Activity

Department of Computer Engineering of Bharati Vidyapeeth's College of Engineering for Women, in association with AWS (Amazon Web Services) Academy, organized online workshops on various courses.

Syllabus of each course is divided into several modules. Complete learning of each module is followed by knowledge check for students. Certificate is awarded to students on successful completion of all such modules with knowledge assessment.

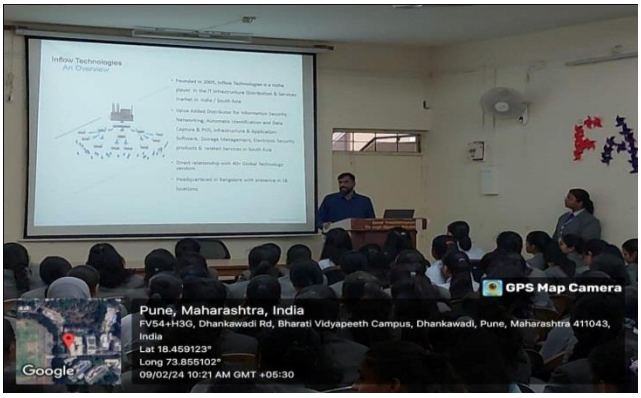
Prof. Dr. Sonali Kadam, Prof. A. P. Kadam, with AWS training partnership conducted these workshops. The details are as follows –

Sr. No	Name of Course	Duration	Total No. of Students Involved	Class
1	Machine Learning for Natural Language Processing	20 Hrs	78	BE
2	AWS Academy Cloud Architect	40 Hrs		
3	AWS Academy Cloud Developing	40 Hrs		
4	AWS Academy Cloud Operation	40 Hrs		

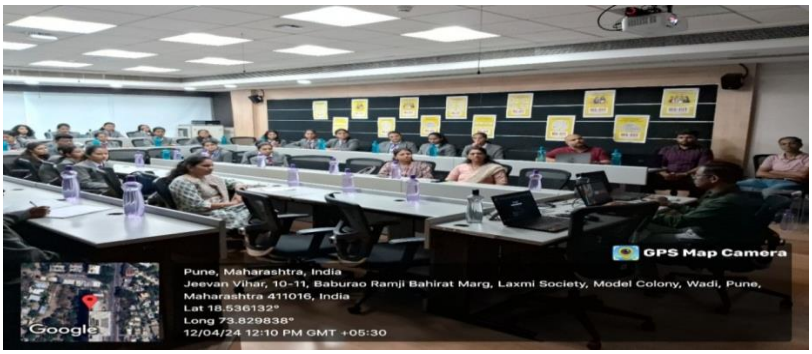
Technical Activities

Seminar on "Placement Assistance for 2024 Batch", by Mr. Aditya Wakodkar, Manager, Seventh Sense Talent Solution, Pune on 10th January 2024 for BE students.

Seminar on “Carrer opportunities in Biomedical Engineering field” conducted by Mrs. Vaishnavi Banke, Medifacts INC, Pune on 10th February 2024 for BE students.



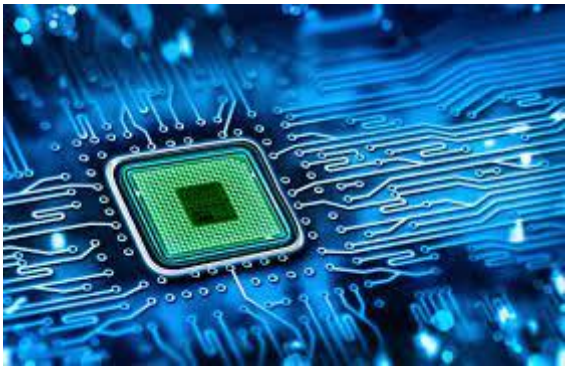
Seminar on " Cyber Security " by Mr. Manish Singh, Manager, Services Sales Inflow Technologies PVT LTD on 9th February 2024 for SE and TE students.



Industrial visit of SE Comp students was organized to the “Maharashtra Knowledge Corporation Limited, Pune” on 12th April 2024.

India’s Semiconductor Surge: Catalyzing a New Era in Technology

India's ambitious journey toward becoming a global hub for semiconductors and display manufacturing is a significant chapter in its technological and economic narrative. The semiconductor industry is the backbone of modern electronics, powering everything from smart phones to advanced computing systems. Meanwhile, display technology is critical for the user interface of a plethora of devices, from televisions and monitors to smart phones and wearable. India's tryst with semiconductor technology began in the late 20th century. The establishment of Bharat Electronics Limited (BEL) in 1954 and the Semiconductor Complex Limited (SCL) in 1983 was among the early efforts to create a domestic semiconductor industry. These initiatives, though significant, faced numerous challenges, including lack of infrastructure, skilled manpower, and limited government support. The announcement of the National Policy on Electronics (NPE) 2019 was a watershed moment. The policy aims to position India as a global hub for Electronics System Design and Manufacturing (ESDM) by promoting domestic manufacturing, increasing exports, and encouraging research and development. One of the most significant initiatives under the NPE 2019 is the Production Linked Incentive (PLI) scheme for the electronics sector. The PLI scheme offers financial incentives to companies engaged in semiconductor and display manufacturing based on their incremental sales and investments. This scheme is designed to attract global giants and boost domestic manufacturing capabilities. Another noteworthy initiative is the Scheme for Promotion of Manufacturing of Electronic Components and Semiconductors (SPECS). This scheme provides financial support for the capital expenditure incurred in setting up semiconductor and display fabrication units. The aim is to develop a robust supply chain and reduce dependence on imports. India's semiconductor ecosystem is evolving rapidly, driven by both government support and private sector initiatives. Several multinational corporations have established their research and development centers in India, recognizing the country's potential. For instance, Intel, AMD, and Qualcomm have significant R&D operations in cities like Bangalore and Hyderabad. The Indian semiconductor design industry is also making strides, with numerous startups and established firms engaged in chip design and verification. Companies like Saankhya Labs, Signal chip and MOS Chip are notable examples of indigenous semiconductor innovation. These firms are developing solutions tailored to the unique requirements of the Indian market, such as rural connectivity and affordable computing. This will significantly contribute to the upcoming rise in the semiconductor industry.



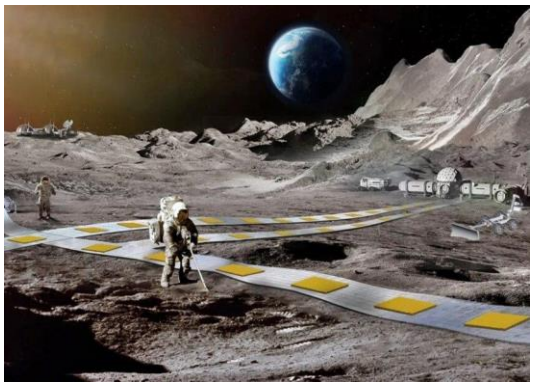
Mr. Akshay Dinkar Kharche

Director, Akshay Embedded Systems Pvt. Ltd.

FLOAT

"Train on Moon"

NASA's quest for sustainable lunar exploration and habitation is taking a futuristic turn with the development of a ground breaking transportation system known as Flexible Levitation on a Track (FLOAT). This innovative project, a part of NASA's Innovative Advanced Concepts (NIAC) program, aims to revolutionize how payloads and resources are transported across the moon's rugged terrain, facilitating the establishment of a permanent lunar base by the 2030s. The FLOAT system is conceived as a series of robotic trains utilizing magnetic levitation technology to move autonomously on flexible tracks. This approach promises a high degree of reliability and efficiency, essential for the harsh and variable conditions of the lunar environment. The design leverages advancements in maglev technology, which eliminates friction by suspending the trains above the tracks using magnetic fields, resulting in smoother and faster transportation compared to traditional wheel-based systems.



The moon presents a unique set of challenges for any transportation system. Its surface is uneven, covered in fine regolith dust, and subject to extreme temperature fluctuations. The FLOAT project addresses these challenges with its adaptable track design and robust engineering. The tracks are designed to be flexible, allowing them to conform to the lunar surface's irregularities. This adaptability is crucial for maintaining stability and ensuring that the trains can traverse the moon's diverse landscapes. Moreover, FLOAT trains are designed to operate autonomously, using advanced navigation systems and artificial intelligence to plan and execute their routes. This autonomy reduces the need for constant human oversight, a critical factor given the communication delays and limited human presence on the moon. The primary purpose of the FLOAT system is to support NASA's long-term goal of a sustainable lunar base. This base would serve as a hub for scientific research, resource extraction, and as a stepping stone for future missions to Mars and beyond. By providing a reliable transportation network, FLOAT would enhance the efficiency of many operations.

In the coming decades, the moon is poised to become a hub of human activity, research, and resource utilization. The Flexible Levitation on a Track (FLOAT) system stands out as a beacon of this future, showcasing NASA's ingenuity and forward-thinking approach to space exploration. By addressing the logistical challenges of lunar transportation, FLOAT not only enhances the feasibility of a lunar base but also sets the stage for humanity's next giant leap into the cosmos.

Mrs. Rufina Fernandes

Teacher Primrose School, Pune
Parent of student,
Ms. Blyana Fernandes

LLMs (Large Language Models)

LLMs are advanced artificial intelligence systems designed to understand and generate human-like text. LLMs are sophisticated computer programs trained on vast amounts of text data from internet. They use complex algorithms to learn the patterns and structures of language, allowing them to generate coherent and contextually relevant text.

Working of LLMs:

LLMs work by processing input text and predicting the next word or sequence of words based on the context provided. They utilize deep learning techniques, particularly neural networks, to analyze and understand the nuances of language.

Applications of LLMs:

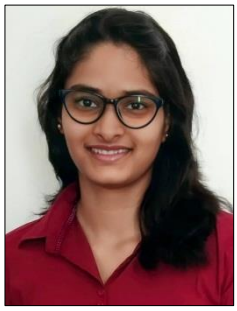
LLMs have a wide range of applications, including:

1. Natural Language Processing (NLP): LLMs are used for tasks such as language translation, sentiment analysis, and text summarization.
2. Content Generation: LLMs can generate human-like text for various purposes including writing articles, composing emails, and generating creative content.
3. Virtual Assistants: LLMs power virtual assistants like chatbots and voice assistants, enabling natural and engaging interactions with users.
4. Information Retrieval: LLMs help in retrieving relevant information from large textdata sets, improving search engines' accuracy and efficiency.

Challenges and Limitations:

Despite their capabilities, LLMs face challenges such as:

1. Bias: LLMs may inadvertently learn and reproduce biases present in the training data leading to biased outputs.
2. Ethical Concerns: The use of LLMs raises ethical questions related to privacy, misinformation, and manipulation.
3. Computational Resources: Training and running LLMs require significant computational resources, limiting their accessibility.



Ms. Shraddha Mishra
BE Comp (Alumna, Batch 2021-22)
Software Engineer (Persistent Systems)

Students' Achievements

Sr. No.	Name of The Student	Class	Achievement
1	Sana Naik	BE	Successfully completed AWS Cloud Practitioner Certification.
2	Ahilya Bandgar		
3	Rajlaxmi Manepatil		
4	Suruchi Bibikar		
5	Tanmayee Chavan		
6	Mitali Rajesh Chavan	TE	Won 1st place and led a PAN India team to victory at the Standard Chartered Diversity Hackathon. The team developed "Invest Saga – Your Gamified Guide to Financial Literacy" in the timespan of less than 48hrs.
7	Sae Jamdade	TE	Successfully completed 6-month internship at the, IIT Roorkee, under Dr. Ravindra V. Kale, and college project guide Prof. Dr. S. P. Kadam, resulting in two National Institute of Hydrology recognized projects for the NIH website: 1. Web Application for cross-sectional area and river discharge calculation. 2. India-Flood Analysis and Mapping System (I-FAMS) using hydrafloods.
8	Apurva Gadilkar		
9	Chahal Ohri		
10	Namrata Rathi		
11	Anushka Chougule	TE	Successfully completed research internship at IIT Patna under the esteemed guidance of Dr. Chandranath Adak Sir and worked on "Deep Visual Product Analysis"
12	Tanvi Mahajan	BE	Won 3rd prize at Global Talent Track Foundations NES Innovation Awards Competition 2024. Selection was done among top 40 projects out of 700 innovative ideas.
13	Ankita Kanawade		
14	Samruddhi Shete		
15	Shweta Jadhav		
16	Kajal Gadekar	BE	Secured 3rd position in national level Project Competition Capgemini CodeX Hackathon, Organized by Capgemini under the guidance of Prof. A. P. Kadam
17	Anuradha Birajdar		
18	Rakshanda Borse		
19	Ankita Tilekar		

Faculty Achievement

Prof. Dr. S. A. Pawar:

Co-Principal Investigator for multidisciplinary project "Design and Development of Dental Implant System using Additive Manufacturing" sanctioned by Bharati Vidyapeeth Deemed to be University Pune with proposal amount of Rs. 1,50,000/- in March 2024.

Department of Engineering Sciences and Allied Engineering

Students' Achievements (A. Y. 2023-24)

Sr. No.	Name of The Student	Name of the Scholarship Received	Amount Received in Rs.
1	Pratiksha Chavan	Lila Poonawala Foundation (LPF)	70,000/-
2	Ankita Keshav Kale	Lila Poonawala Foundation (LPF)	70,000/-
3	Shraddha Somanand Kale	Lila Poonawala Foundation (LPF)	50,000/-
4	Komal Arjun Nimbalkar	Lila Poonawala Foundation (LPF)	50,000/-
5	Samruddhi Yashwant Patil	Lila Poonawala Foundation (LPF)	70,000/-
6	Sakshi Gulab Sanas	Lila Poonawala Foundation (LPF)	60,000/-
7	Harshada Vasantrao Yadav	Lila Poonawala Foundation (LPF)	60,000/-
8	Aradhana Atmaram Bankar	Lila Poonawala Foundation (LPF)	70,000/-
9	Sakshi Anil Bhople	Lila Poonawala Foundation (LPF)	70,000/-
10	Akshada Satish Kabule	Lila Poonawala Foundation (LPF)	70,000/-
11	Dhanashree Vijay Kadam	Lila Poonawala Foundation (LPF)	70,000/-
12	Bhakti Anil Neharkar	Lila Poonawala Foundation (LPF)	70,000/-
13	Tanvi Sudhir Kadu	Lila Poonawala Foundation (LPF)	65,000/-
14	Manasi Ajay Talele	Lila Poonawala Foundation (LPF)	70,000/-
15	Kanchan Sachin Ugale	Lila Poonawala Foundation (LPF)	70,000/-
16	Kranti Atul Dumbre	Lila Poonawala Foundation (LPF)	70,000/-
17	Prapti Santosh Gawande	Lila Poonawala Foundation (LPF)	60,000/-
18	Sanika Namadev Ghogare	Lila Poonawala Foundation (LPF)	50,000/-
19	Shrusti Sachin Kunjir	Lila Poonawala Foundation (LPF)	60,000/-
20	Nandini Prabhashankar Pandey	Lila Poonawala Foundation (LPF)	55000/-
21	Rutuja Ani Ugale	Lila Poonawala Foundation (LPF)	70,000/-

22	Kashish Anil Kumar Godhwani	Lila Poonawala Foundation (LPF)	50,000/-
23	Shreya Bajirao Kamekar	Lila Poonawala Foundation (LPF)	50,000/-
24	Shravani Mohan Maradane	Lila Poonawala Foundation (LPF)	70,000/-
25	Shruti Ramesh Patil	Lila Poonawala Foundation (LPF)	45000/-
26	Tanvi Sahebrao Pawar	Lila Poonawala Foundation (LPF)	50,000/-
27	Pranita Shinde	Katalyst Scholarship	15,000/-
28	Prajakta Gawade	Katalyst Scholarship	15,000/-
29	Shraddha Dinde	Katalyst Scholarship	15,000/-
30	Prachi Kasliwal	Katalyst Scholarship	15,000/-
31	Dhanashree Memane	Katalyst Scholarship	15,000/-
32	Saniya Sondkar	Katalyst Scholarship	15,000/-
33	Shreya Kamerikar	Katalyst Scholarship	15,000/-
34	Sanika Magar	Katalyst Scholarship	15,000/-
35	Deepshikha Sharma	Katalyst Scholarship	15,000/-
36	Priyanka Dukale	Katalyst Scholorship	15,000/-
37	Tanuja Londhe	Katalyst Scholorship	15,000/-
38	Pranali Jadhav	Katalyst Scholorship	15,000/-
39	Prapti Gawande	Katalyst Scholorship	15,000/-
40	Nandini Pandey	Katalyst Scholorship	15,000/-
41	Tanuja Khartode	Katalyst Scholorship	15,000/-
42	Pratiksha Pandhare	Katalyst Scholorship	15,000/-
43	Shravani Mardane	Katalyst Scholorship	15,000/-
44	Tanvi Kudu	Katalyst Scholorship	15,000/-
45	Sanika Namadev Ghogare	Persistent Foundation	50,000/-

Esteemed Recruiters

Placements from January 2024 to May 2024

SR. NO.	NAME OF THE STUDENT	COMPANY
1	RENUKA UDAY SARMOKDAM	BNY Mellon
2	MAHAK CHAWLA	WESTERN UNION
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