Hon'ble Dr. Patangraoji Kadam Saheb
Founder, Bharati Vidyapeeth

BLESSINGS

Bharati Vidyapeeth's

College of Engineering for Women, Pune

Participation of women in technology is an important aspect in social and economic development of the nation. It is a critical constituent in the process of improving the quality of life of women themselves. When women have economic empowerment, it is a way for others to see them as equal members of society. Through this, they achieve more self-respect and confidence by their contributions to their communities. As women play key roles in social transformation, Hon'ble Dr. Patangraoji Kadam Saheb established Bharati Vidyapeeth's College of Engineering for Women, Pune in June 2000 with the vision, "Women empowerment through Technical Education" and providd opportunity to women for higher education in the field of technology. The institute was started exclusively for women and it is running with 100% women students. Establishing and running Women Engineering College really contributes to social transformation through dynamic education which is the vision of Bharati Vidyapeeth.

Bharati Vidyapeeth's College of Engineering for Women, Pune

Pune-Satara Road, Dhankawadi, Pune 411043
Recognized by AICTE, New Delhi, DTE Mumbai,Savitribai Phule Pune University,
Accredited by NAAC
Id No.: PU/PN/Engg./150/2000, DTE College Code: EN6285
Phone: (020)24371684, (020)24361732 Fax: (020) 24372210
Email: coewpune@bharatividyapeeth.edu, Website: http://coewpune.bharatividyapeeth.edu

Undergraduate Programme

Sr. No.	Course	Intake	Course Code
1	B.E. Electronics and Telecommunication Engg. (E & TC)	120	628537250F
2	B.E. Computer Engg. (CE)	120	628524550F
3	B.E. Information Technology (IT)	60	628524650F

Post Graduate Programme

Sr. No.	Course	Intake	Course Code
1	M.E. (E & TC-VLSI & Embedded System)	9	628534150F

Vision:

Women Empowerment through Technical Education

Mission:

- Develop women students to rise to their full potential.
- Impart knowledge and prepare competent engineers.

Special Features:

1. Received “Best Women College of the Year 2019” Award.
2. Recipient of “College of Substance” Award.
3. The oldest engineering college “exclusively for women”.
4. All government scholarships are applicable for eligible students.
5. Placement opportunities in multinational companies with 100% assistance.
6. Excellent university results and tradition of consistent university rank holders.
7. MOUs with reputed industries and academia.
8. On campus hostel facility with 24 × 7 security.
9. DTE approved e-Scrutiny centre for admissions.

Facebook: https://www.facebook.com/Bharati-Vidyapeeth-College-of-Engineering-for-Women-Pune-1599060517007121
Instagram: https://instagram.com/bvcoew_pune?igshid=ep1a85ikhj6s

Principal's Message

Prof. Dr. Pradeep Jadhav
Principal

Dear Stakeholders,

Greetings from BVCOEW, Pune!

I hope you are all well and in good health. I am delighted to announce that BVCOEW is celebrating its Silver Jubilee this year, a significant milestone that reflects our dedication to excellence in education and our enduring impact on the community.

I am very pleased to release the e-newsletter "Blessings..." Volume 6, Issue 2, A.Y. 2023-24. This e-newsletter serves as an excellent medium for showcasing the technical endeavours and accomplishments of both students and faculty throughout the semester. I am particularly content and glad that we were able to organise so many technical events for our students during this period. Participation in these activities enriches students' educational experiences and contributes to their personal growth.

I highly appreciate the team efforts of the Coordinator, Prof. Dr. Deepali Godse, all Chief Editors, and Editors for making this e-newsletter a grand success. I also extend my best wishes to the students' editorial team. The e-newsletter is the outcome of great teamwork, and we always believe that "Alone, we can do so little; together, we can do so much".

Internal Quality Assurance Cell (IQAC)

IQAC Objectives:

- To imbibe quality environment at institute in all academic and administrative processes.
- To be instrumental in review of teaching learning process, structures, methodologies and student centric methods for achieving best educational environment.

Roles and responsibilities of IQAC:

- Keeping regular updates of NAAC and other quality improvement circulars.
- Conducting regular meetings of IQAC.
- Preparing Strategic plan of the institute.
- Preparation and submission of Annual Quality Assurance Report (AQAR) yearly.
- Maintaining academic records and conducting various audits at required intervals.
- Taking review of updating and updation of hardware and software requirements and internet facilities.
- Updating feedback forms as per guidelines from regulatory bodies.
- Providing guidelines for implementing ERP.
- Organizing various technical and nontechnical events.
- Preparation of reports of various activities for quality improvement.

Members List:

Sr. No.	Name of the IQAC Member	Designation	Position
1	Prof. Dr. P. V. Jadhav	Head of the Institute	Chairperson
2	Dr. K. D. Jadhav	Joint Secretary of Bharati Vidyapeeth	Member of Management
3	Dr. S. F. Patil	Executive Director of Bharati Vidyapeeth	Member of Management
4	Prof. Dr. S. R. Patil	HOD, E & TC	Teacher Representative
5	Prof. Mrs. Khot S.T	Training cell Coordinator	Teacher Representative
6	Prof. Dr. V. R. Pawar	Academic & Research Coordinator	Teacher Representative
7	Prof. Dr. S. M. Rajbhoj	Industry institute Interaction	Teacher Representative
8	Prof. Ms. K.D.Mahajan	Alumni Coordinator	Teacher Representative
9	Prof. D. D. Pukale	HOD, Computer Engineering	Teacher Representative
10	Prof. Mrs. P. D. Kale	Placement cell Coordinator	Teacher Representative
11	Prof. Dr. D. A. Godse	HOD, Information Technology	Teacher Representative
12	Prof. Dr. K. A. Malgi	ICT & IT Infrastructure Coordinator	Teacher Representative
13	Prof. Dr. A. M. Pawar	HOD, Engineering Sciences and Allied Engineering	Teacher Representative
14	Mrs. Vaishali Kadam	Office Superintendent	Admin. Representative
15	Dr. V. M. Mohite	Librarian	Admin. Representative
16	Mr.Nityanand Prabhutendolkar	Chief Technical Officer,ErgenTechnovationPrt. Ltd.	Industry Representative
17	Mr. Sanjaykumar Gupta	Parent	Parent Representative
18	Ms. ShitalPatil	Alumna (IT)	Alumni Representative
19	Ms. Khushi Mittal	Student (E & TC)	Student Representative
20	Prof. Dr. S. S. Chorage	Professor (E & TC)	Coordinator of the IQAC

Coordinator's Message

Prof. Dr. D. A. Godse
Newsletter Coordinator

Dear Students, Faculty, Staff, Alumni, Employers, Parents and Well-wishers,

Greetings!

We are very fortunate to have the blessings of our honourable founder, Dr. Patangraoji Kadam Saheb, with us at Bharati Vidyapeeth forever.
As we embark on a remarkable milestone, our esteemed institution, Bharati Vidyapeeth's College of Engineering for Women, Pune, is celebrating 25 years of excellence in technical education for empowering women. It gives me immense pleasure to welcome you to this edition of our semester-wise e-newsletter. This semester, we continue to uphold our commitment to fostering a vibrant academic environment enriched with technical activities that propel our students towards becoming future leaders and innovators. From insightful seminars to hands-on workshops, and from cutting-edge research to industry collaborations, every endeavor undertaken at BVCOEW, Pune, reflects our unwavering dedication to nurturing talent and pushing the boundaries of knowledge.I would like to extend my sincere thanks to our Principal, Prof. Dr. Pradeep Jadhav, all Heads of Department, faculty, staff and beloved students for their involvement in organizing and conducting various technical activities. I truly appreciate dedicated and systematic efforts of the editorial team members of the e-newsletter.

Thank you everyone for being a part of our journey. Here's to 25 years of brilliance, and too many more milestones ahead!

We always believe, "Teamwork makes the dream work".

Department of Electronics and Telecommunication Engineering

Vision

To develop women professionals to become a valuable resource for industry and society through E&TC Engineering.

Mission

- *To provide quality and value based education for women in the field of E&TC Engineering.*
- *To train women to keep pace with rapidly changing technological needs of industry and research.*

Program Educational Objectives (PEOs)

- *Ability to apply electronics knowledge, to identify formulates and solve Engineering problems.*
- *Acquire knowledge to find out workable solutions in the field of Telecommunication.*
- *Exhibit programming skills with the use of various software tools.*
- *Inculcate continuous learning through interdisciplinary approach*

Program Outcomes (POs)

On completion of the program graduate will be able to

1. ***Engineering knowledge**: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.*
2. ***Problem analysis**: Identify, formulate, review research literature, and analyse complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.*

3. ***Design/development of solutions****: Design solutions for complex engineering problems and: design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations*
4. ***Conduct investigations of complex problems****: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions*
5. ***Modern tool usage****: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations*
6. ***The engineer and society****: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice*
7. ***Ethics****: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice*
8. ***Environment and sustainability****: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development*
9. ***Individual and teamwork****: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.*
10. ***Communication****: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.*
11. ***Project management and finance****: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.*
12. ***Life-long learning****: Recognize the need for and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.*

Program Specific Outcomes (PSOs)

The graduate will be able to

- *Give techniques and solutions by using acquired knowledge and skills.*
- *Design and develop Electronics & and telecommunication-based systems.*
- *Create, select and adapt techniques, resources and tools with understanding of associated limitations.*
- *Identify and address their own needs in the changing world through lifelong learning.*

Department of Electronics and Telecommunication Engineering

Head of Department's Message

Prof. Dr. S. R. Patil

Head of Electronics and Telecommunication Engineering Department

I am delighted to welcome you to the department of Electronics and Telecommunication Engineering where we believe not only nurturing the academic excellence but also holistic development through diverse experiences and activities. The department offers a top-notch learning environment with committed, dedicated and innovation demonstrated staff members who are driven to help students enhance their technical talents. The department understands the importance of fostering creativity, leadership, and teamwork skills, which are essential for success in today's dynamic world. That's why department places great emphasis on motivating our students to participate in a wide range of activities beyond the traditional curriculum. From academics to sports and arts, community service to entrepreneurship, our students have the opportunity to explore their interests, develop their talents, and discover their passions. Through these diverse activities, they not only enhance their skills but also build character, resilience, and a sense of responsibility. Students are also encouraged to take part in a variety of non-technical and technical events, paper presentations and national project competition along with academics.

I want to take this opportunity to express my gratitude to the team of newsletter hard work, passion, and collaborative spirit. Together, we have achieved remarkable milestones and made significant contributions to our field. I am proud to be a part of this incredible team which will undoubtedly benefit the Institute's stakeholders and be environmentally friendly. After this little overview, let me extend a warm greeting to everyone for this edition of the newsletter, Volume 6, Issue 2 with a famous quote by Bard of Bengal, Mr. Rabindranath Tagore, "The highest Education is that which does not merely gives us information but makes our life in harmony with all existence.

Technical Festival 2024 “Technophilia”

Inaugural program of Technical Festival 2024 “Technophilia”

Technophilia 2024, an annual technical extravaganza, illuminated the academic calendar of Bharati Vidyapeeth’s College of Engineering for Women, Pune. This year, the event radiated with the theme "Tech-Passion Fuels Innovation for a Better Tomorrow" underscoring the pivotal role of technology in shaping our collective future. The inaugural ceremony of Technophilia 2024 was graced by esteemed personalities, adding prestige to the occasion. Hon'ble Padmashree Mrs. Lila Poonawalla, Chairperson of the Lila Poonawala Foundation, adorned the event as the Chief Guest. Additionally, Hon’ble Mrs.Swapnali Kadam, Chairperson of Bharati Vidyapeeth Rabindranath Tagore School of Excellence, Pune, honored us as the Guest of Honour. Technophilia 2024 showcased a diverse array of technical and non-technical events, creating an immersive experience for participants. The technical segment included Project Exhibition, Poster Presentation, Startup Idea Competition, Circuit Building, Coding Competition, and Technical Quiz. Complementing these, non-technical events such as Rangoli Competition, T-shirt Painting, Reel Making Competition, and a special Photo Exhibition for faculty and staff members added colors and vibrancy to the festival. Under the guidance of Prof. Dr. D. A. Godse as the Convener, and the diligent coordination of Prof. A. V. Kanade and Prof. K.V. Patil, Technophilia 2024 manifested into a grand success. Ms.Suhani Havaldar from Third Year IT Department led the organizing committee of students as the General Secretary, ably supported by Co-General Secretaries, Ms.Sanchita Sawai and Ms.Saee Datar. The allure of Technophilia 2024 resonated far and wide, as evidenced by the remarkable participation of students from various colleges. With a total of 342 registrations across all events, the festival buzzed with energy and fervor. The commitment and zeal displayed by participants, coupled with meticulous planning by the organizing committee, culminated in the resounding success of this year's edition. Technophilia 2024 stands as a testament to the power of technology and innovation in shaping a brighter tomorrow. Through a blend of technical prowess, creative expression, and collaborative spirit, the event fostered an environment conducive to learning, growth, and camaraderie. As we bid adieu to yet another memorable chapter of Technophilia, we eagerly anticipate the future, fueled by the passion for technology and the pursuit of excellence. The collective efforts of faculty and supporting staff under the great support, guidance and innovative ideas of dynamic Principal, Prof. Dr. Pradeep Jadhav proved “Teamwork makes the dreamwork”.

Seminar on
“Semiconductor Technology”

Seminar on “Semiconductor Technology” by Dr. R. B. Ghongade on 13th March 2024 for BE E&TC students.

The Electronics and Telecommunication department organised a seminar on the topic "Semiconductor Technology" on March 13, 2024 for final year (B.E.) students which was delivered by Prof. Dr. R. B. Ghongade (Professor, Bharati Vidyapeeth Deemed to be University College of Engineering). Principal, Prof. Dr. P.V. Jadhav welcomed the speaker for the session, delivered the seminar's opening remarks and commenced a discussion with the students on the latest developments in the VLSI area.

Dr. R. B. Ghongade emphasized the primary important elements of this seminar. He began by giving a brief introduction and discussing his professional background. Prime Minister Hon’ble Mr. Narendra Modi initiated three semiconductor factories in India on March 13, 2024. These facilities represent a major advancement in India's capacity to produce semiconductors. They are anticipated to foster innovation, boost employment prospects in the semiconductor industry and aid in the expansion of India's electronics industry.

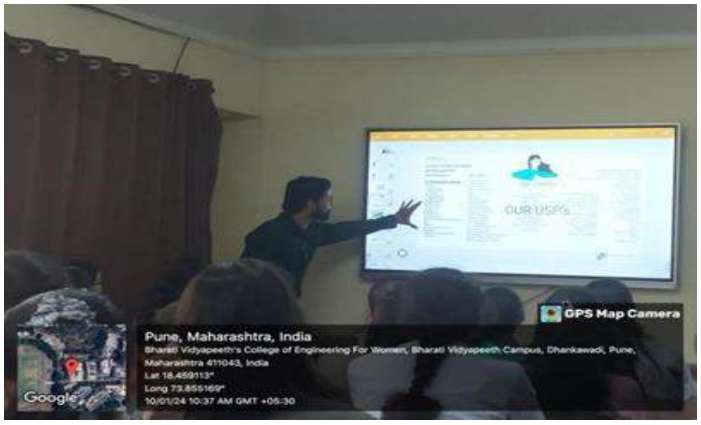
The construction of these semiconductor facilities is consistent with the government's goal of turning India into a major worldwide centre for the production of electronics. It emphasizes the nation's dedication to creating an ecosystem that is both technologically sophisticated and self-sufficient.

These facilities will be essential in lessening India's reliance on semiconductor and related component imports. India hopes to fortify its digital infrastructure and improve its standing in the global supply chain by encouraging home grown chip manufacture.

Prof. Dr. R. B. Ghongade's vast expertise delighted the BE students. He declared at the end of the seminar that this endeavour will undoubtedly advance technology and create an environment that encourages investment in the electronics sector.

Prof. Dr. S. S. Chorage (Vice Principal, Academics), Prof. Dr. A. M. Pawar (Vice Principal, Administration), Prof. V. S. Karambelkar and Prof. S. M. Patil attended the session. This insightful presentation enlightened over eighty B.E. E&TC students.

Technical Activities

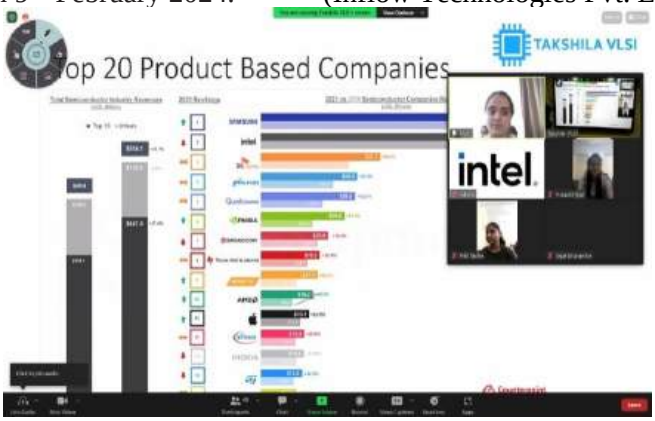


BE: Seminar on "Placement Assistance for 2024 batch" by Mr. Aditya Wakodkar (SevenSence) on 10th January 2024.

BE: Seminar on "Career Opportunities in Biomedical Engineering Field" by Mrs. Vaishnavi Banke (Medi facts inc, Pune) on 1st February 2024.

SE: Webinar on "Cyber Security" by Mr. Manish Singh (Inflow Technologies Pvt. Ltd.) on 9nd February 2024.

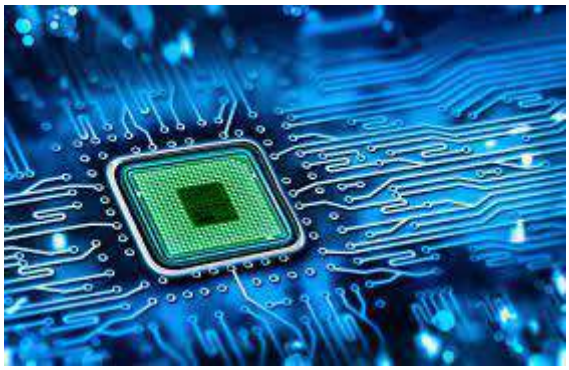
TE: Webinar on "Cyber Security" by Mr. Manish Singh (Inflow Technologies Pvt. Ltd.) on 9nd February 2024.



TE,BE: Webinar on " Job opportunities in VLSI Semi Conductor industry " by Mr. Laxmi Narsimha (Takshila VLSI Institute) on 28th March 2024.

India’s Semiconductor Surge: Catalyzing a New Era in Technology

India's ambitious journey toward becoming a global hub for semiconductors and display manufacturing is a significant chapter in its technological and economic narrative. The semiconductor industry is the backbone of modern electronics, powering everything from smart phones to advanced computing systems. Meanwhile, display technology is critical for the user interface of a plethora of devices, from televisions and monitors to smart phones and wearable. India's tryst with semiconductor technology began in the late 20th century. The establishment of Bharat Electronics Limited (BEL) in 1954 and the Semiconductor Complex Limited (SCL) in 1983 was among the early efforts to create a domestic semiconductor industry. These initiatives, though significant, faced numerous challenges, including lack of infrastructure, skilled manpower, and limited government support. The announcement of the National Policy on Electronics (NPE) 2019 was a watershed moment. The policy aims to position India as a global hub for Electronics System Design and Manufacturing (ESDM) by promoting domestic manufacturing, increasing exports, and encouraging research and development. One of the most significant initiatives under the NPE 2019 is the Production Linked Incentive (PLI) scheme for the electronics sector. The PLI scheme offers financial incentives to companies engaged in semiconductor and display manufacturing based on their incremental sales and investments. This scheme is designed to attract global giants and boost domestic manufacturing capabilities. Another noteworthy initiative is the Scheme for Promotion of Manufacturing of Electronic Components and Semiconductors (SPECS). This scheme provides financial support for the capital expenditure incurred in setting up semiconductor and display fabrication units. The aim is to develop a robust supply chain and reduce dependence on imports. India's semiconductor ecosystem is evolving rapidly, driven by both government support and private sector initiatives. Several multinational corporations have established their research and development centers in India, recognizing the country's potential. For instance, Intel, AMD, and Qualcomm have significant R&D operations in cities like Bangalore and Hyderabad. The Indian semiconductor design industry is also making strides, with numerous startups and established firms engaged in chip design and verification. Companies like Saankhya Labs, Signal chip and MOS Chip are notable examples of indigenous semiconductor innovation. These firms are developing solutions tailored to the unique requirements of the Indian market, such as rural connectivity and affordable computing. This will significantly contribute to the upcoming rise in the semiconductor industry.



Mr.Akshay Dinkar Kharche

Director, Akshay Embedded Systems Pvt. Ltd.

FLOAT
"Train on Moon"

NASA's quest for sustainable lunar exploration and habitation is taking a futuristic turn with the development of a groundbreaking transportation system known as Flexible Levitation on a Track (FLOAT). This innovative project, a part of NASA's Innovative Advanced Concepts (NIAC) program, aims to revolutionize how payloads and resources are transported across the moon's rugged terrain, facilitating the establishment of a permanent lunar base by the 2030s. The FLOAT system is conceived as a series of robotic trains utilizing magnetic levitation technology to move autonomously on flexible tracks. This approach promises a high degree of reliability and efficiency, essential for the harsh and variable conditions of the lunar environment. The design leverages advancements in maglev technology, which eliminates friction by suspending the trains above the tracks using magnetic fields, resulting in smoother and faster transportation compared to traditional wheel-based systems.

The moon presents a unique set of challenges for any transportation system. Its surface is uneven, covered in fine regolith dust, and subject to extreme temperature fluctuations. The FLOAT project addresses these challenges with its adaptable track design and robust engineering. The tracks are designed to be flexible, allowing them to conform to the lunar surface's irregularities. This adaptability is crucial for maintaining stability and ensuring that the trains can traverse the moon's diverse landscapes. Moreover, FLOAT trains are designed to operate autonomously, using advanced navigation systems and artificial intelligence to plan and execute their routes. This autonomy reduces the need for constant human oversight, a critical factor given the communication delays and limited human presence on the moon. The primary purpose of the FLOAT system is to support NASA's long-term goal of a sustainable lunar base. This base would serve as a hub for scientific research, resource extraction, and as a stepping stone for future missions to Mars and beyond. By providing a reliable transportation network, FLOAT would enhance the efficiency of many operations.

In the coming decades, the moon is poised to become a hub of human activity, research, and resource utilization. The Flexible Levitation on a Track (FLOAT) system stands out as a beacon of this future, showcasing NASA's ingenuity and forward-thinking approach to space exploration. By addressing the logistical challenges of lunar transportation, FLOAT not only enhances the feasibility of a lunar base but also sets the stage for humanity's next giant leap into the cosmos.

Mrs. Rufina Fernandes

Teacher Primrose School, Pune
Parent of student,
Ms. Blyana Fernandes

Understanding Generative AI: A Comprehensive Overview

Generative AI, an innovative branch of artificial intelligence, focuses on creating new content such as images, text, or even music, based on patterns it learns from existing data. This fascinating technology has garnered significant attention due to its ability to generate highly realistic and creative outputs that mimic human-like creations. In this article, we delve in to the fundamentals of generative AI, explore its advantages and limitations, discuss its applications in various fields and provide examples showcasing its real-world impact. Generative AI, also known as generative modeling, involves training algorithms to generate new data that resembles a given dataset. Unlike traditional AI systems that primarily focus on classification or prediction tasks, generative AI aims to produce original content by learning and understanding the underlying patterns and structures within the data. The advantages of Generative AI are Creativity Enhancement where Generative AI enables the creation of novel content, ranging from images and videos to text and music, fostering creativity in various domains, Data Augmentation which can generate synthetic data to augment existing datasets, helping improve the performance and robustness of machine learning models, personalization wherein Generative models can be tailored to individual preferences, offering personalized recommendations, advertisements, and user experiences and Exploration of Unseen Spaces by exploring the latent space of data distributions, generative models can unveil new insights and generate previously unseen examples.

Applications of Generative AI:

Art and Design: Generative AI is revolutionizing the art and design industry by enabling the creation of unique artworks, 3D models, and visual effects.

Content Generation: It powers content creation platforms, such as text generators for articles, product descriptions, and social media posts.

Healthcare: Generative models aid in medical image generation, drug discovery, and personalized medicine, facilitating advancements in health career search and diagnostics.

Gaming: In the gaming industry, generative AI is utilized for procedural content generation, creating immersive environments, characters, and narratives.

Fashion and Retail: Retailers leverage generative AI for virtual try-on experiences, trend prediction, and personalized product recommendations, enhancing the online shopping experience.

In conclusion, generative AI holds immense promise in revolutionizing various industries from art and design to healthcare and gaming. Despite facing challenges such as quality control and ethical considerations, the continued advancements in generative modeling are paving the way for a future where AI driven creativity and innovation thrive.

Ms. Tanushree Desale

(2023 Batch Alumna)
Graduate Trainee
VOIS(Vodafone Intelligent Solutions)

Achievements

Staffs' Achievement

Prof. Dr. S. R. Patil

A Candidate completed Doctarate of Philosophy in "Abnormal Event Detection and localization of Smart Surveillance" from SPPU on 19 April 2024.

A Patent published on the topic "Deep learning based abnormal events detection and localization system for smart surveillance and its method therof" with application no. 202321080269.

Prof. Dr. S. S. Chorage

A Candidate completed Doctarate of Philosophy in "Interpretation of Electroencephalography (ECG) signals for efficient classification of motor tasks in brain computer interface" from SPPU on 5 January 2024.

A Patent granted on the topic "Sensing and simulation system and method for exhaust air particles exhausting from device" with patent number 483975 and application no. 202221027698.

Prof. Dr. V. R. Pawar

Awarded with Academic Excellence award by BVCOEW on 5 April 2024.

Prof. Dr. S. L. Kore

A Patent proceeded to grant on the topic "A Novel Multiclass-multistage writer verification using hybrid approach in spatial domain and transform domain" with patent number 483975 and application no. 201721016687.

A Patent published on the topic "Model for improving the taste and quality of chapatti" with application no. 202421037725.

Prof. S. A. Itkarkar

Appointed as Team Leader by Savitribai Phule Pune University SDO and NSS unit during "Yuva-Sangam Phase –III" cultural educational tour of students of Assam during 21 January 2024 to 28 January 2024.

Prof. Dr. S.A. Dhole

A Patent granted on the topic "Smart Biosensor Device to Detect Lung Cancer" with design no. 6322973.

A Patent granted on the topic "Smart desk to identify learning Disabilities" with design no. 380135-001.

Prof. S. M. Bhilegaonkar

A Patent published on the topic "Model Dual Plane Substrate Integrated Waveguide Monopulse Tracking Antenna (SIW-MTA)" with application no. 382637-001.

Prof. Dr. R. M. Shamalik

Successfully completed the degree of Doctorate of Philosophy in the topic "Design and Development of Real time system for Human Gesture Detection with depth estimation" under the guidance of Prof. Dr. Koli Sanjay offered by Savitribai Phule Pune University.

A Patent granted on the topic "Defab: a system for gesture detection using foreground background separation with depth Estimation" with patent number 511565 and application no. 202121049257.

Department of Engineering Sciences and Allied Engineering

Students' Achievement (2023-2024)

Sr. No.	Name of the Student	Name of the Scholarship Received	Amount Received in Rs.
1	Pratiksha Chavan	Lila Poonawala Foundation (LPF)	70000
2	Ankita Keshav Kale	Lila Poonawala Foundation (LPF)	70000
3	Shraddha Somanand Kale	Lila Poonawala Foundation (LPF)	50000
4	Komal Arjun Nimbalkar	Lila Poonawala Foundation (LPF)	50000
5	Samruddhi Yashwant Patil	Lila Poonawala Foundation (LPF)	70000
6	Sakshi Gulab Sanas	Lila Poonawala Foundation (LPF)	60000
7	Harshada Vasantrao Yadav	Lila Poonawala Foundation (LPF)	60000
8	Aradhana Atmaram Bankar	Lila Poonawala Foundation (LPF)	70000
9	Sakshi Anil Bhople	Lila Poonawala Foundation (LPF)	70000
10	Akshada Satish Kabule	Lila Poonawala Foundation (LPF)	70000
11	Dhanashree Vijay Kadam	Lila Poonawala Foundation (LPF)	70000
12	Bhakti Anil Neharkar	Lila Poonawala Foundation (LPF)	70000
13	Tanvi Sudhir Kadu	Lila Poonawala Foundation (LPF)	65000
14	Manasi Ajay Talele	Lila Poonawala Foundation (LPF)	70000
15	Kanchan Sachin Ugale	Lila Poonawala Foundation (LPF)	70000
16	Kranti Atul Dumbre	Lila Poonawala Foundation (LPF)	70000
17	Prapti Santosh Gawande	Lila Poonawala Foundation (LPF)	60000
18	Sanika Namadev Ghogare	Lila Poonawala Foundation (LPF)	50000
19	Shrusti Sachin Kunjir	Lila Poonawala Foundation (LPF)	60000
20	Nandini Prabhashankar Pandey	Lila Poonawala Foundation (LPF)	55000
21	Rutuja Ani Ugale	Lila Poonawala Foundation (LPF)	70000
22	Kashish Anil Kumar Godhwani	Lila Poonawala Foundation (LPF)	50000
23	Shreya Bajirao Kamekar	Lila Poonawala Foundation (LPF)	50000

24	Shravani Mohan Maradane	Lila Poonawala Foundation (LPF)	70000
25	Shruti Ramesh Patil	Lila Poonawala Foundation (LPF)	45000
26	Tanvi Sahebrao Pawar	Lila Poonawala Foundation (LPF)	50000
27	Pranita Shinde	Katalyst Scholarship	15000
28	Prajakta Gawade	Katalyst Scholarship	15000
29	Shraddha Dinde	Katalyst Scholarship	15000
30	Prachi Kasliwal	Katalyst Scholarship	15000
31	Dhanashree Memane	Katalyst Scholarship	15000
32	Saniya Sondkar	Katalyst Scholarship	15000
33	Shreya Kamerikar	Katalyst Scholarship	15000
34	Sanika Magar	Katalyst Scholarship	15000
35	Deepshikha Sharma	Katalyst Scholarship	15000
36	Priyanka Dukale	Katalyst Scholorship	15000
37	Tanuja Londhe	Katalyst Scholorship	15000
38	Pranali Jadhav	Katalyst Scholorship	15000
39	Prapti Gawande	Katalyst Scholorship	15000
40	Nandini Pandey	Katalyst Scholorship	15000
41	Tanuja Khartode	Katalyst Scholorship	15000
42	Pratiksha Pandhare	Katalyst Scholorship	15000
43	Shravani Mardane	Katalyst Scholorship	15000
44	Tanvi Kudu	Katalyst Scholorship	15000
45	Sanika Namadev Ghogare	Persistent Foundation	50000

Our Esteemed Recruiters

Placements from December 2023 to May 2024

Sr. No.	Name of the Student	Company	Sr. No.	Name of the Student	Company
1	YASHASHRI KULKARNI	WESTERN UNION	13	PATHAK MANSI RAJENDRA	SPARK MINDA
2	SAYALI MANOJ KSHIRSAGAR	UNO MINDA	14	AVANTIKA SUNIL BHOSALE	SPARK MINDA
3	ZAINAB AJIJ SHAIKH	UNO MINDA	15	SANJANA ASHOK KHADAKABHAVI	SPARK MINDA
4	MEDHAVI RADHAKRISHNA PURANIK	UNO MINDA	16	ANUJA AMIT KADAM	SPARK MINDA
5	SHWETA SHANTARAM SHIVALE	UNO MINDA	17	KSHIRSAGAR SHWETA CHANDRASHEKHAR	SPARK MINDA
6	SHUBHANGI SHRIMALSING PARDESHI	UNO MINDA	18	PRITI RAJARAM TARATE	SPARK MINDA
7	VAISHNAVI RAKESH PATANGE	SPARK MINDA	19	AKSHATA HANUMANT SHINDE	SPARK MINDA
8	RAJSHREE KISHOR PATIL	SPARK MINDA	20	YASHSHRI VISHNU NAGARGOJE	PUBILICIS SUPIENT
9	DHANASHRI JAGDISH RATHI	SPARK MINDA	21	SHRUTI SINGH	CAPGEMINI
10	DISHA PRASHANT GOSKE	SPARK MINDA	22	SAKSHI BALASAHEB SHIRKE	CAPGEMINI
11	SHIVANI SHIVKANT MUGALE	SPARK MINDA	23	ANJALI PANDURANG MESHRAM	CAPGEMINI
12	VAIBHAVI SURYAKANT JARANDE	SPARK MINDA			

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